# Specification of APERTIF Polyphase Filter Bank in CλaSH

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#### Introduction

- What is CλaSH?
  - Functional Language and Compiler for Concurrent Digital Hardware Design
- Motivation?
  - Testing CλaSH on real life complex application
- \* Why APERTIF Polyphase filter bank?
  - Strict specification on Throughput, Area and clock frequency

#### CλaSH

- \* A functional language and compiler for digital hardware design
- \* On the lowest level, everything is a Mealy machine f(s,i) = (s',o)
- A CλaSH description is purely structural i.e. all operations are performed in a single clock cycle
- Simulation is cycle accurate



mac (State s) 
$$(a, b) = (State s', out)$$
  
where  
 $s' = s + a * b$   
 $out = s'$ 



#### **APERTIF** project





- APERTIF Polyphase Filter bank
  - Increasing field of view of Westerbork telescope using small array
  - \* Each antenna in the array requires a polyphase filter bank
  - \* Goals:  $F_{clk} = 200 MHz$  and throughput = 800 MS/s

- APERTIF Polyphase
   Filter bank
  - Polyphase FIR filter
  - \* FFT



- Design method
- Polyphase filter
- FFT pipeline

Design method

- Design whole Architecture first in plain Haskell
- \* Perform small modification such that the code is accepted by  $C\lambda aSH$ 
  - \* Lists are replaced by vectors: lists with fixed length
  - Fixed point representation for numbers
- \* A clear division between structure and low level hardware details



#### Polyphase Filter



FIRM-1

#### Polyphase Filter



FIRM-1

#### Polyphase Filter



FIRM-1

Parallel Polyphase Filter



 $parpfs \ csss \ states \ inps = (states', outs)$ where  $res = zipWith3 \ pfs \ csss \ states \ inps$  $(states', outs) = unzip \ res$ 





 $\begin{array}{ll} fir :: (Vector \ D16 \ S) \rightarrow (State \ (Vector \ D16 \ S)) \rightarrow S \ \rightarrow (State \ (Vector \ D16 \ S), S) \\ fir \ cs \ (State \ us) \ inp = (State \ us, \ out) \\ \hline \mathbf{where} \\ us \ = inp \ +\gg us \\ ws \ = vzipWith \ fpmult \ uscs \\ out \ = vfoldl \ (+) \ 0 \ ws \end{array}$ 

$$\begin{array}{l} fir :: (Vector \ D16 \ S)) \rightarrow (State \ (Vector \ D16 \ S)) \rightarrow S \rightarrow (State \ (Vector \ D16 \ S)), S) \\ fir \ cs \ (State \ us) \ inp = (State \ us, \ out) \\ \hline \mathbf{where} \\ us \ = \ inp \ +\gg \ us \\ ws \ = \ vzipWith \ fpmult \ uscs \\ out \ = \ vfoldl \ (+) \ 0 \ ws \end{array}$$

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$$in \longrightarrow u_{0}$$

$$in \longrightarrow v_{0}$$

$$in$$

$$\begin{array}{l} fir :: (Vector \ D1(S)) \rightarrow (State \ (Vector \ D1(S)) - S) \rightarrow (State \ (Vector \ D1(S), S)) \\ fir \ cs \ (State \ us) \ up = (State \ us, \ out) \\ \hline \mathbf{where} \\ us \ = \ inp \ + \gg \ us \\ ws \ = \ vzipWith \ fpmult \ uscs \\ out \ = \ vfoldl \ (+) \ 0 \ ws \end{array}$$

FFT pipeline



FFT pipeline



\* FFT is implemented using pipeline

Two types of butterflies and Complex multiplier

FFT pipeline





bf2i (cntr, $lst$ )	inp = ((cntr', lst'), out)
where	
n	= length lst
cntr'	= (cntr + 1) 'mod' n
lst'	$= lstin + \gg lst$
(out, lstin	$(a) = \mathbf{if} \ cntr \ge n$
`	then $(lstout + inp, lstout - inp)$
	else (lstout , inp )
lstout	= last lst

 $\cap \cap$ 1111 2 8 WS WS 4 1111 1111 1111 1111 BF2II BF2I BF2II BF2I 1111 \* inp -> out UU

 $cmult \ ws \ cntr \ inp = (cntr', out)$ where  $n = length \ ws$  $cntr' = (cntr + 1) \ `mod` \ n$ 

$$w = ws! cntr'$$

$$out = inp * w$$

FFT pipeline

FFT pipeline



 $\begin{array}{l} \textit{fftbb ws (bf1state, bf2state, cmstate) inp} = ((\textit{bf1state', bf2state', cmstate'}), out) \\ \textbf{where} \\ (\textit{bf1state', a}) &= \textit{bf2i bf1state inp} \\ (\textit{bf2state', b}) &= \textit{bf2ii bf2state a} \\ (\textit{cmstate', out}) &= \textit{cmult ws cmstate b} \end{array}$ 

FFT pipeline



 $\begin{array}{l} \textit{fftchain} (ws1, ws2, \ldots) (bb1state, bb2state, \ldots) \textit{inp} = ((bb1state', bb2state', \ldots), out) \\ \textbf{where} \\ (bb1state', d1) = \textit{fftbb} ws1 bb1state \textit{inp} \\ (bb2state', d2) = \textit{fftbb} ws2 bb2state d1 \\ & \circ \\ & \circ \\ & (bbNstate', out) = \textit{fftbb} wsN bbNstate d9 \end{array}$ 

## Describing the PFB fft BF2I: Haskell $\rightarrow$ CAaSH

 $bf2i\_clash\ (cntr, lst)\ inp = ((cntr', lst'), out)$ where  $n = vlength\ lst$  cntr' = cntr + 1  $lst' = lstin + \gg lst$   $(out, lstin) = \mathbf{if}\ cntr \ge n$   $\mathbf{then}\ (lstout + inp, lstout - inp)$   $lstout = vlast\ lst$ 

# Describing the PFB FFT BF2I: Haskell → CλaSH

$$bf2i \ (cntr, lst) \ inp = ((cntr', lst'), out)$$
where
$$n = length \ lst$$

$$cntr' = (cntr + 1) \ 'mod' \ n$$

$$lst' = lstin \ +\gg \ lst$$

$$(out, lstin) = \mathbf{if} \ cntr \ge n$$

$$\mathbf{then} \ (lstout + inp, lstout - inp)$$

$$\mathbf{else} \ (lstout \ , inp \ )$$

$$lstout = last \ lst$$

$$bf2i\_clash\ (cntr, lst)\ inp = ((cntr', lst'), out)$$
where
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$$cntr' = cntr + 1$$

$$lst' = lstin + \gg lst$$

$$(out, lstin) = \mathbf{if}\ cntr \ge n$$

$$\mathbf{then}\ (lstout + inp, lstout - inp)$$

$$else\ (lstout, inp)$$

$$lstout = vlast\ lst$$

# Describing the PFB FFT BF2I: Haskell → CλaSH

bf2i	(cntr, lst) i	inp = ((cntr', lst'), out)
$\mathbf{W}$	here	
	n	= length lst
(	cntr'	= (cntr + 1) `mod` n
	lst'	$= lstin + \gg lst$
	(out, lstin)	$=$ if $cntr \ge n$
		then $(lstout + inp, lstout - inp)$
		else (lstout , inp
	lstout	= last lst

$$bf2i\_clash\ (cntr, lst)\ inp = ((cntr', lst'), out)$$
where
$$n = vlength\ lst$$

$$cntr' = cntr + 1$$

$$lst' = lstin + > lst$$

$$(out, lstin) = \mathbf{if}\ cntr \ge n$$

$$\mathbf{then}\ (lstout + inp, lstout - inp)$$

$$lstout = vlast\ lst$$

#### Results

- Polyphase filter bank has been fully implemented using CλaSH
- \* Simulation shows that the PFB operates correctly
- \* Synthesis revealed some limitations of the current compiler

	Polyphase filter(256 elements)	1k-points FFT
Logic utilization	91%	6%
blockRAMS	0	0
DSP blocks	128	70
Max. <i>F</i> <sub>clk</sub>	114 MHz	195 MHz

#### Conclusions

- \* The complete Polyphase Filter Bank has been implemented
- Haskell code needs only small modifications before it is accepted by the CλaSH compiler
- \* The description is purely parallel (structural) and cycle accurate
- Shortcomings of CλaSH compiler
  - Large coefficient vectors not supported
  - BlockRAM not supported, limiting F<sub>clk</sub>

#### Future Work

- \* Develop area vs time time trade off based on functional description
- Improvements for the CλaSH compiler
  - Support for blockRAMs on FPGA
  - Support for memory initialization files for coefficient vectors

#### Questions ?