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CPA 2013 Fringe, Napier University, 25 August, 2013

[†] plus non-blocking barriers and performance ...

The Joy of Sync

Process oriented design ...

Synchronous communications ...

Synchronous barriers ...

Mutually assured destruction ...

Non-blocking barriers ...

Performance ...



(a) a network of three processes, connected by four internal (hidden) and three external channels.



(b) three processes sharing the writing end of a channel to a server process.



(c) three processes sharing the writing end of a channel to a bank of servers sharing the reading end.



(d) n processes enrolled on a shared barrier (any process synchronising must wait for all to synchronise).



(a) a network of three processes, connected by four internal (hidden) and three external channels.

```
CHAN BYTE a, b, c, d:

PAR

foo (in?, left!, a?, b!, c!)

bar (a!, b?, d!)

merge (c?, d?, right!)
```



```
PROC thing (CHAN INT in?, left!, right!)
CHAN BYTE a, b, c, d:
PAR
foo (in?, left!, a?, b!, c!)
bar (a!, b?, d!)
merge (c?, d?, right!)
;
```



```
PROC thing (CHAN INT in?, left!, right!)
CHAN BYTE a, b, c, d:
PAR
foo (in?, left!, a?, b!, c!)
bar (a!, b?, d!)
merge (c?, d?, right!)
```





Like **foo**, **bar** and **merge** previously, **thing** is a process that can be used as a component in another network.

Concurrent systems have structure – networks within networks. We must be able to express this! And we can ... 🕲 🕲 🕲



(b) three processes sharing the writing end of a channel to a server process.

```
SHARED ! CHAN SOME.SERVICE C:

PAR

circle (c!)

triangle (c!)

square (c!)

server (c?)
```





(c) three processes sharing the writing end of a channel to a bank of servers sharing the reading end.

```
BARRIER b:
PAR i = 0 FOR n ENROLL b
p (i, b)
```



(d) n processes enrolled on a shared barrier (any process synchronising must wait for all to synchronise).

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Synchronised Communication



A may **write** on **c** at any time, but has to wait for a **read**.

B may *read* from *c* at any time, but has to wait for a *write*.



Synchronised Communication



Only when both **A** and **B** are ready can the communication proceed over the channel **c**.





Benefit

Once the writer has written, it knows the reader has read

OK: plenty of other processes to run and ultra-fast context switch (comparable to a procedure call)

Careful

- Writer blocks if reader is not ready
- Lots of deadlock possibilities

OK: work with (a small set of) synchronisation patterns for which we have proven safety theorems



If there is no discipline on when **A** and **B** communicate, then **A** may commit to output on **c**, followed by **B** on **d** ... or vice-versa. Either way, neither are listening and both are stuck. Same happens if both commit to input.



Client-Server Pattern



client: makes a *request* any time, then commits to taking *reply*.

server: always accepts a *request* (within some bounded time), then always makes a *reply* (within some bounded time). It may make requests itself, as a *client* to other *servers*.



Client-Server Pattern



client: makes a *request* any time, then commits to taking *reply*.

server: always accepts a *request* (within some bounded time), then always makes a *reply* (within some bounded time). It may make requests itself, as a *client* to other *servers*.

Symbology: this represents a client-server relation. It points to the server and allows a 2-way conversation (initiated by the client)

Client-Server Pattern

A server may have many clients ...



Only one *client* at a time converses with the *server*. They form an orderly queue. Still no deadlock possible – and no *client* starvation. No polling on the queue, so no livelock either.

Client-Server Theorem

A *client-server* system that has no cycles in its *client-server* relations is deadlock, livelock and starvation free.



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Barriers

The occam- π **BARRIER** type corresponds to a multiway CSP event, though some higher level design patterns (such as *resignation*) have been built in.



Basic CSP semantics apply. When a process synchronises on a barrier, it blocks until all other processes enrolled on the barrier have also synchronised. Once the barrier has completed (i.e. all enrolled processes have synchronised), all blocked processes are rescheduled for execution.

Barriers

The occam-π BARRIER type corresponds to a multiway CSP event, though some higher level design patterns (such as resignation) have been built in.





Processes may synchronise on more than one barrier:



BARRIER b, c: PAR i = 0 FOR n ENROLL b, c worker (i, b, c)

To synchronise on a barrier:

Barriers

Barriers are commonly used to synchronise multiple **phases** of computation between a set of processes. Within each phase, other synchronisations (channel/barrier) may take place:





Of course, only one barrier is actually needed to synchronise the phases in this example:



But it's safer programming for each phase to be synchronised

by its own barrier ...



occam-π BARRIER Synchronisation is safe in the sense that enrollment and resignation are automatically managed. A process may synchronise on a BARRIER if and only if it is enrolled.

Try to break this rule ... your program won't compile. There are zero memory and run-time costs to enforce it. ③

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Mutually Assured Destruction

Two processes are given, at the same time, their own task to complete; we are satisfied with the completion of either one of them; whichever process finishes first interrupts the other and reports its completion; the one that is interrupted abandons its task and reports that fact.

Such requirements are common in control systems, robotics, e-commerce, model-checking, ...

	- Drive rover vehicle forwards target meters.
e.g.	- Look out for <i>Martians</i> .
	- Stop and report when either is achieved.




























average sensor data interval = 10 ms (randomised) average sensor inputs per service = 100 (randomised)

Soak Testing



Ran for 30 days (approx. 2.5m trials): PASSED

25-Aug-2013

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average sensor data interval = 10 ms (varying) average sensor inputs per service = 100 (varying)

In Service



Ran for 2 years (approx. 64m trials): DEADLOCKED



25-Aug-2013

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Should have asked for a model check ...



A trace leading to deadlock is provided:

<moveCommand, motorSensor, searchCommand, cameraSensor>

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A trace leading to deadlock is provided:

<moveCommand, motorSensor, searchCommand, cameraSensor>

















```
average sensor data interval = 10 ms (randomised)
average sensor inputs per service = 100 (randomised)
>
average service time = 1 second
kill window = 100 nanoseconds (approx.)
→
chance of kill window overlap (deadlock) = 1/10,000,000
                                                            Kill Window
→
time before 50% chance of deadlock = 90 days (approx.)
       INT x:
       sensor ? x
          SEQ
                  update local state with x (depends on mode)
            . . .
            IF
                    task complete
                 SEQ
                   killYou ! kill
                   report ! me
                   running := FALSE
              TRUE
                 SKIP
     2
```



Mutually Assured Destruction (revised implementation)

Communication between the monitors is mostly a *one-way* kill (from killer to killed). Deadlock happens when both turn killer – *two-way* communications.

Idea: make communication between the monitors *always two-way* – either a kill in both directions (should both tasks complete around the same time) or a kill in one direction followed by an **ack** in the other (which will be most of the time).

Claim: this eliminates all deadlock (at the cost of an extra ack).







```
PROC monitor' (VAL INT mode, CHAN INT command?, sensor?,
               CHAN REPORT' report!,
               CHAN KILL' killYou!, killMe?)
 WHILE TRUE
    PRI ALT
      INT target:
      command ? target
                                   -- service requested
        service' (mode, target, sensor?, report!,
                  killYou!, killMe?)
      INT X:
                                    -- accept and discard
      sensor ? x
        SKIP
2
```





Previously ...









Previously ...













Previously ...





Better ask for a model check ...





Soak Testing



Only for confidence boosting – it will not deadlock (assuming compiler, run-time kernel, microprocessor are OK)

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This will not deadlock

(assuming compiler, run-time kernel, microprocessor are OK)



If the channels were finitely buffered (with capacity greater than zero), the deadlock found with synchronous (i.e. zero-buffered) channels would not happen – both monitors would complete their kills, reports and service routines. 25-Aug-2013 Copyright P.H.Welch, (2013)

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If the channels were finitely buffered, deadlock is still possible – but less likely (exponentially) with increasing buffer size. Infinitely expandable buffer capacity would be needed to eliminate deadlock from the basic algorithm. For practical purposes, I would feel safe with a capacity of 3. 25-Aug-2013



However, there is a nasty problem. If both monitors send a kill, neither is taken and they remain lurking in the buffered channels. Some time in the next service cycle, both will strike and the services will be erroneously aborted. 25-Aug-2013 Copyright P.H.Welch, (2013)



This could be overcome by counting cycles and **sequence numbering** the **kill** signals: just ignore any **kill** with a number less than the current count. This adds complexity and run-time overhead.



This could be overcome by counting cycles and **sequence numbering** the **kill** signals: just ignore any **kill** with a number less than the current count. Further, this only works if the processes engaged in MAD are in lock-step (which they are in this scenario, but not in general).



Alternatively, the mess could be sorted out by the **Controller** process. When/if it gets two **me** reports from the monitors, it tells each monitor (as part of its next command) to read and discard an incoming **kill**. Again, this adds complexity – we shouldn't have a mess to clean up!



Alternatively, the mess could be sorted out by the **Controller** process. When/if it gets two **me** reports from the monitors, it tells each monitor (as part of its next command) to read and discard an incoming **kill**. Further, this assumes a **Controller**, which processes engaged in **MAD** may not have. 25-Aug-2013

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Non-Blocking Barriers

Recently (2012) introduced to MPI, *non-blocking barrier synchronisation* seems, at first glance, a contradiction of terms ... the whole point of a *barrier* is to *block* until all parties are there!

When we have completed our work before a *barrier*, we normally synchronise on it – thereby notifying that we are there and waiting for the others.







Processes may synchronise on more than one barrier:



BARRIER b, c: PAR i = 0 FOR n ENROLL b, c worker (i, b, c)

To synchronise on a barrier:





Barriers are commonly used to synchronise multiple **phases** of computation between a set of processes. Within each phase, other synchronisations (channel/barrier) may take place:


Non-Blocking Barriers

Recently (2012) introduced to MPI, *non-blocking barrier synchronisation* seems, at first glance, a contradiction of terms ... the whole point of a *barrier* is to *block* until all parties are there!

When we have completed our work before a *barrier*, we normally synchronise on it – thereby notifying that we are there and waiting for the others.

However, if there is something we can be getting on with that does not disturb our fellow *synchronisers*, (e.g. preparatory work for the phase following the *barrier*), it would be good to be able to do so. Only when we need stuff that depends on the other *synchronisers*, should we have to wait for them.

Blocking Barrier Sync (MPI)



Blocking Barrier Sync (occam-\pi)



Non-Blocking Barrier Sync (MPI)



Non-Blocking Barrier Sync ($0-\pi$)



Non-Blocking Barrier Sync ($o-\pi$)



The **SYNC** registers that we have arrived at the barrier and lets all move forward when the rest arrive. In parallel with the above, we get on with our **preparatory work**.

When our **preparatory** work is complete, if all the others have reached the barrier, the **SYNC** will have completed – so the **PAR** completes and we immediately move on to **phase 1**. And we have not delayed the others. ©

When our **preparatory** work is complete, if not all the others have reached the barrier, the **sync** will not have completed. We wait for the others at the **sync** before moving on to **phase 1** – as we must! ©

Non-Blocking Barrier Sync ($0-\pi$)



The **sync** registers that we have arrived at the barrier and lets all move forward when the rest arrive. In parallel with the above, we get an Our preparatory work. 🙂

Nothing new in occam-T is needed for this. When our preparatory work is others have reached the barrier, the **SYNC** will be so the **par** completes and we And we have not delayed the others. immediately mov

paratory work is complete, if not all the others have Wł reached the barrier, the **SYNC** will not have completed. We wait for the others at the **SYNC** before moving on to **phase 1** – as we must! ©

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Performance

Take a ring of n processes ...

Each process connects to all ...

In parallel, each process sends and receives **m** messages (e.g. its *id number*) to all, including itself ...

That's mn² messages ...

How long per message?



P(0)

P(n-1)

Performance

* A. Bate: "Scalable Performance for Scala Message-Passing Concurrency", CPA 2013, pp. 113-132, Open Channel Publishing.

Andrew's "Say Hello to Everyone" Benchmark (v1) *

```
2n<sup>2</sup> processes
[n][n]CHAN INT c:
                                  mn<sup>2</sup> messages
PAR i = 0 FOR n
 PAR
   PAR j = 0 FOR n -- for each j in parallel,
      SEQ k = 0 FOR m
                           -- send m messages (i to j)
       c[i][j] ! i
    PAR j = 0 FOR n -- for each j in parallel,
                           -- receive m messages (j to i)
      seq k = 0 for m
       INT X:
        SEO
          c[j][i] ? x
         ASSERT (x = j) -- sanity check
```

Performance

* A. Bate: "Scalable Performance for Scala Message-Passing Concurrency", CPA 2013, pp. 113-132, Open Channel Publishing.

Andrew's "Say Hello to Everyone" Benchmark (v2) *



Performance

P(n-1)

P(0)

P(1

P(3)

Take a ring of N processes ...

Each	The following observations were made using KRoC 1.5.0-pre5, Ubuntu 11.04 (natty) on an Intel i7 quad-core processor with
In par	hyperthreading (i.e. 8 virtual cores), running at 2 MHz.
and re its id)	The benchmark timings were averaged from 10 separate runs, with negligible variance.

That's mn² messages ...

How long per message?





























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Any questions?